

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,895,596 B1
APPLICATION NO. : 08/997622
DATED : May 17, 2005
INVENTOR(S) : William E. Salzer

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 4, Line 46, Claim No. 1:

Change "second memory circuits for selecting between the fit" to --second memory circuits for selecting between the first--

In Column 4, Line 63, Claim No. 5:

Change "amount of data stored in tile first memory circuit" to --amount of data stored in the first memory circuit--

In Column 5, Line 10, Claim No. 8:

Change "storing the data stream including storing data of the fast" to --storing the data stream including storing data of the first--

Signed and Sealed this

Tenth Day of June, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office